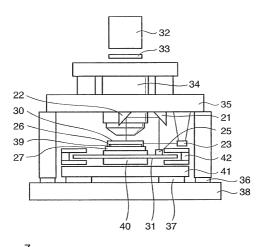
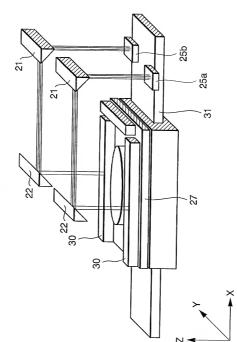


FIG. 2







F1G. 3

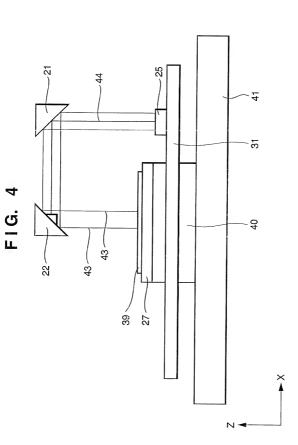
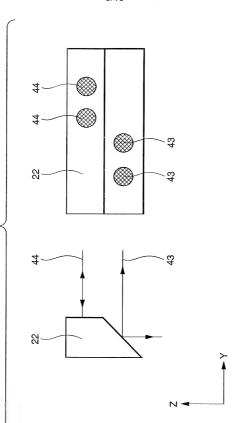
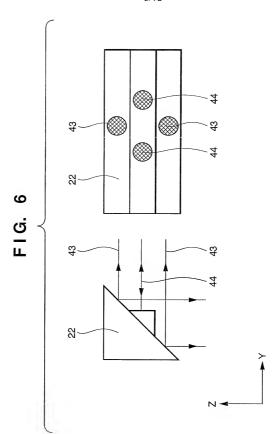


FIG. 5





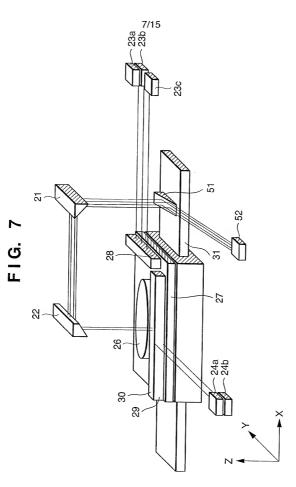


FIG. 8

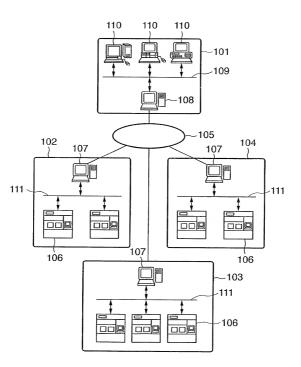


FIG. 9

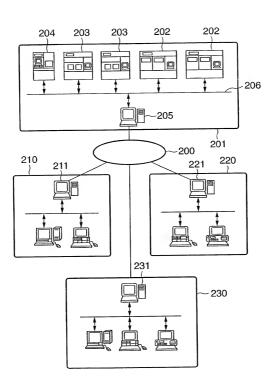


FIG. 10

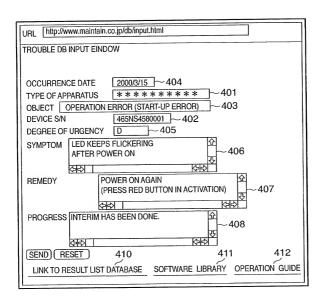
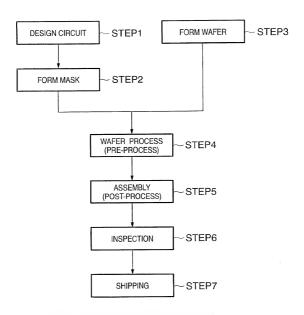


FIG. 11



SEMICONDUCTOR DEVICE MANUFACTURING FLOW

FIG. 12

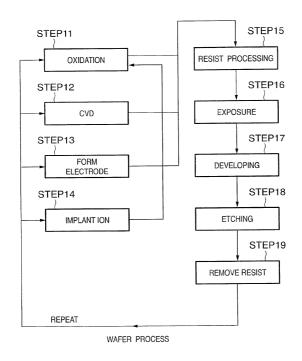


FIG. 13

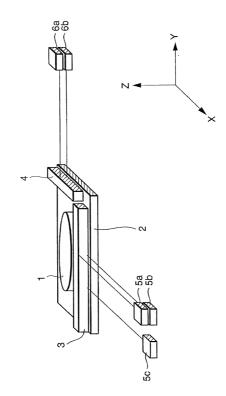


FIG. 14

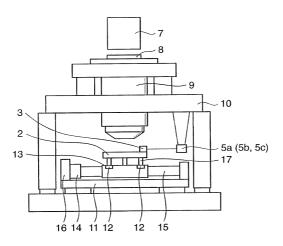




FIG. 15

